Stamp Adaptor 2

Technical Reference

Stamp Adaptor 2: Technical Reference

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1. Stamp Adaptor 2

1.1. Description

The Stamp Adaptor 2 functions as interface board between Stamp9G20 and Panel-Card EVB. It may also be used for prototyping without the EVB. It has the connectors for Panel-Card EVB (X21, X22), a wrapfield for direct access to all pins of Stamp9G20 (X11, X12) and connectors for connecting the Stamp9G20. Additionally it implemts a Davicom® DM9161BIEP ethernet physical interface design, which may be used for customer designs as well.

1.2. Connector Pin Assignments

The pin assignments for the wrapfield are identical with the pins of Stamp9G20. The pin assignment for X21/X22 are below:

1	VCC			DSR0	ISI D2	PB22	2	1	VCC			SCK2	RXD4	PA30	2
3	PB25	ISI D5	RI0	RXD0		PB5	4	3	PA31	TXD4	SCK0	RXD2		PB9	4
5	PB4		TXD0	DTR0	ISI D4	PB24	6	5	PB8		TXD2	TXD1	TCLK1	PB6	6
7	PB26	ISI D6	RTS0	CTS0	ISI D7	PB27	8	7	PA4	MCDB2	RTS2	CTS2	MCDB1	PA5	8
9	PB23	ISI D3	DCD0	GND			10	9	PB7	TCLK2	RXD1	GND			10
11	PB14		DRXD	DTXD		PB15	12	11	PA23	ETX2	TWD	TWCK	ETX3	PA24	12
13	PA7		MCCDA	MCCK		PA8	14	13	VCC			GND			14
15	PA6		MCDA0	MCDA1		PA9	16	15	PB0	TIOA3	SPI1MISO	SPI1MOSI	TIOB3	PB1	16
17	PA10	ETX2	MCDA2	MCDA3	ETX3	PA11	18	17	PB2	TIOA4	SPI1CLK	SPI1CS1	TIOA5	PB3	18
19	VCC			GND			20	19	PA0	MCDB0	SPI1CS2		SPI1CS3	PC3	20
21	USB Host A-			USB Host B-			22	21							22
23	USB Host A+			USB Host B+			24	23							24
25	USB Device-			USB Device+			26	25							26
27	PA22	ETXER	ADTRG	/RESET			28	27	VCC			GND			28
29	VCC			GND			30	29	TDI			TMS			30
31	PB17	TCLK4	TF0	RF0	ISI D1	PB21	32	31	TDO			TCK			32
33	PB16	TCLK3	TK0	RK0	ISI D0	PB20	34	33	ELED_L			ELED_S			34
35	PB18	TIOB4	TD0	RD0	TIOB5	PB19	36	35	ETX+			ETX-			36
37	BMS			WKUP			38	37	ERX+			ERX-			38
39	SHDN			VBATT			40	39	POE1			POE2			40

Table 1.1. Pin Assignment and Multiplexing X21

Table 1.2. Pin Assignment and Multiplexing X22

1.3. Stamp Adaptor 2 Dimensions

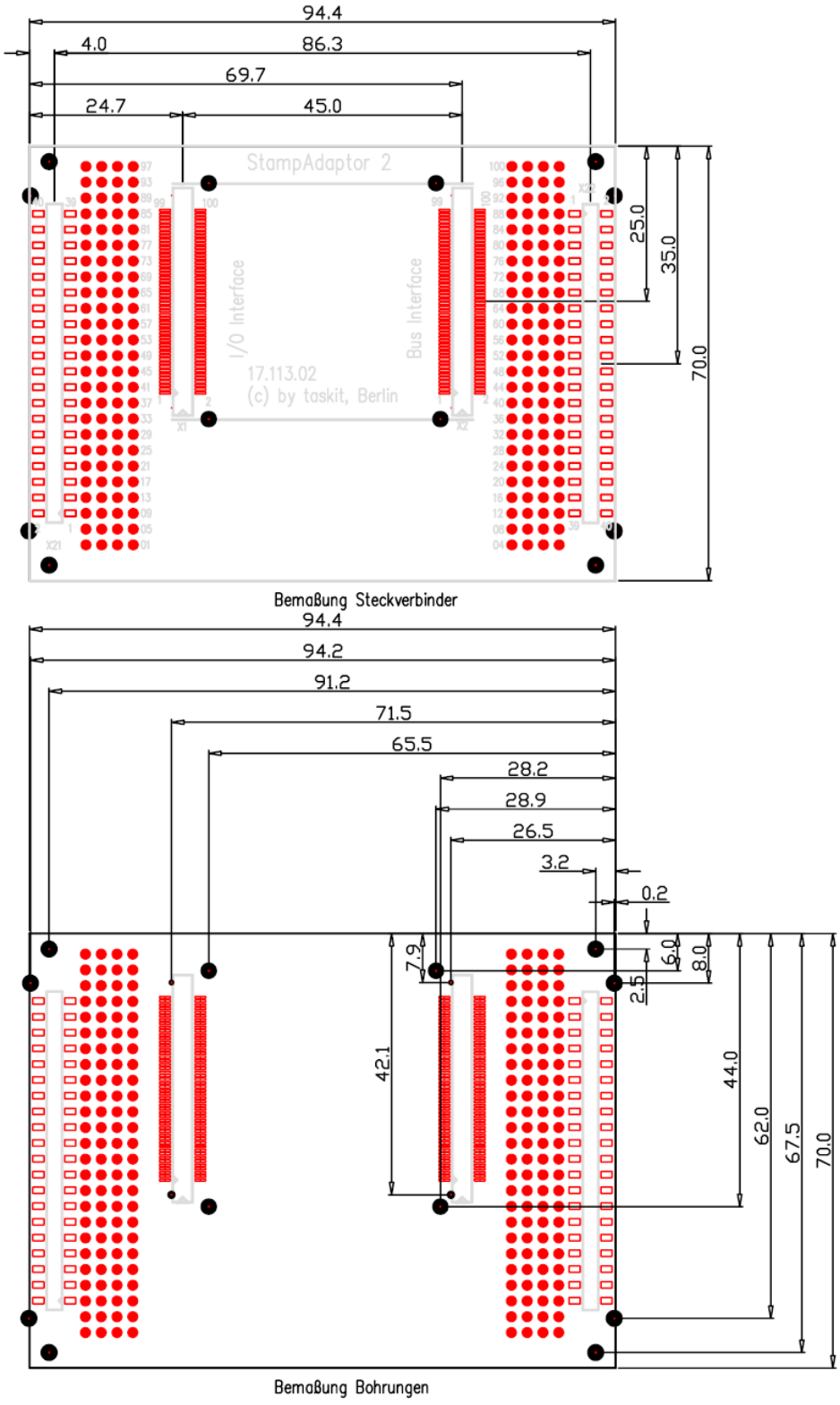

Figure 1.1. Stamp Adaptor 2 Dimensions

1.4. Stamp Adaptor 2 Schematics

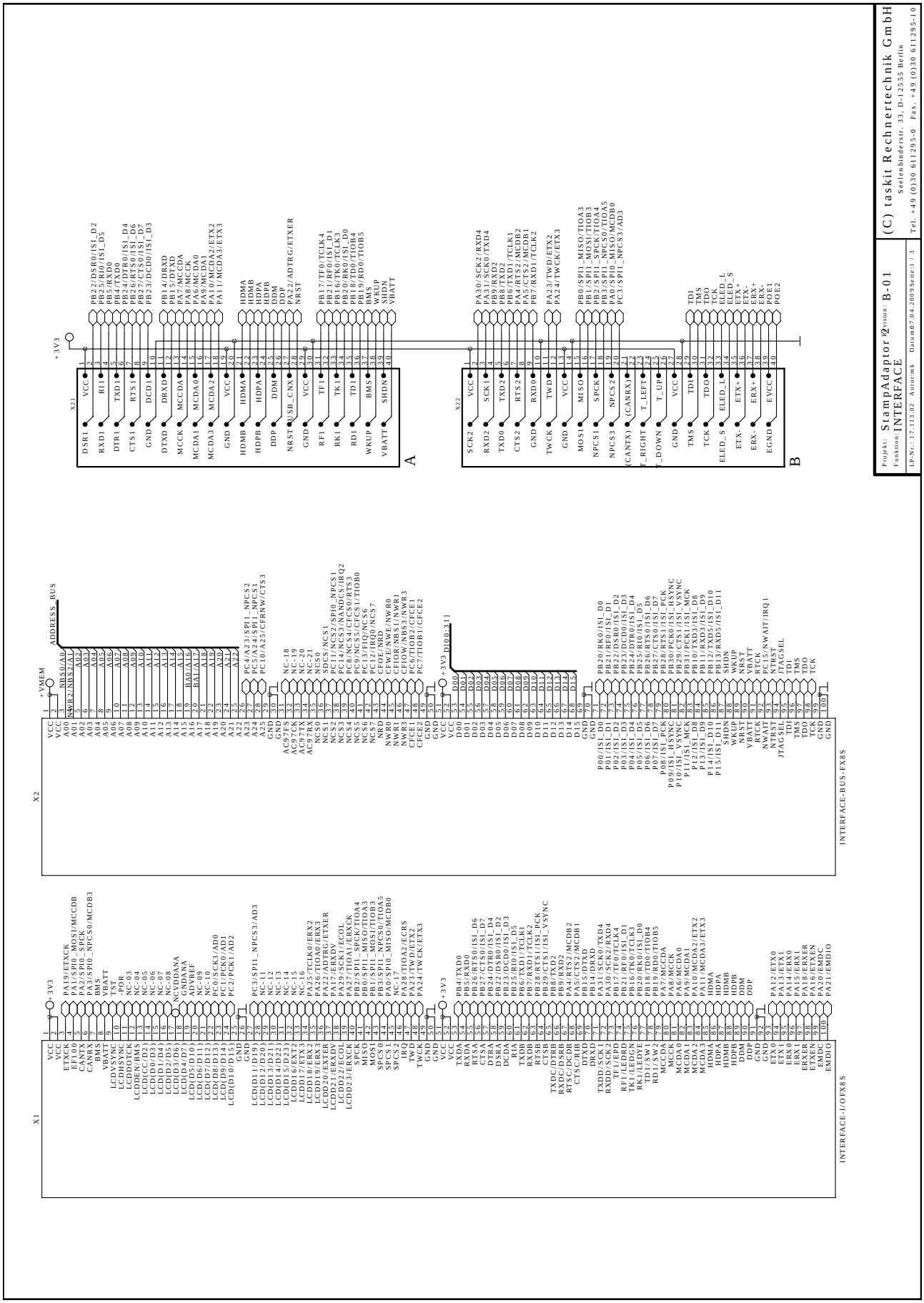

Figure 1.2. Stamp Adaptor 2 Bus Interface

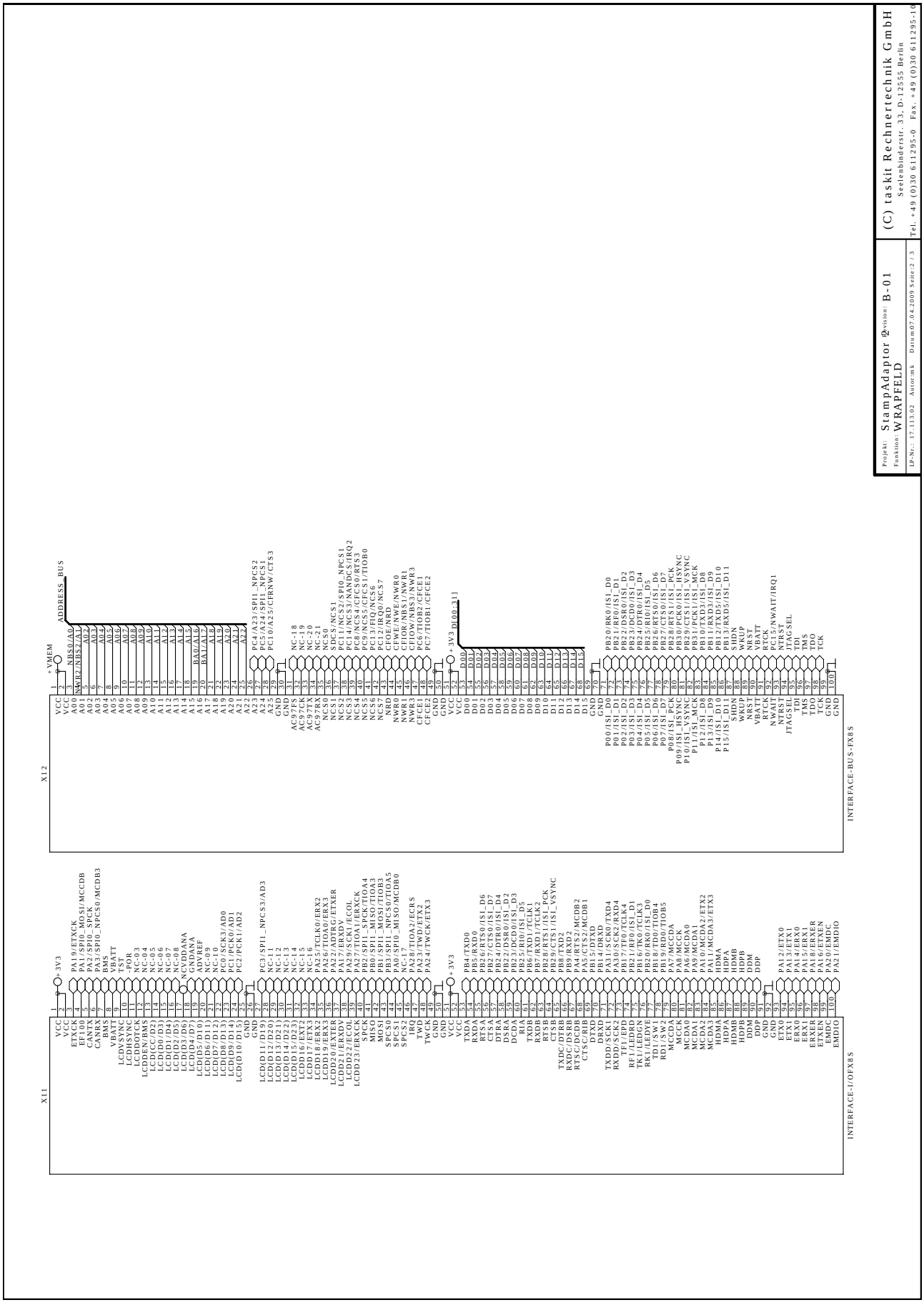

Figure 1.3. Stamp Adaptor 2 Wrapfield

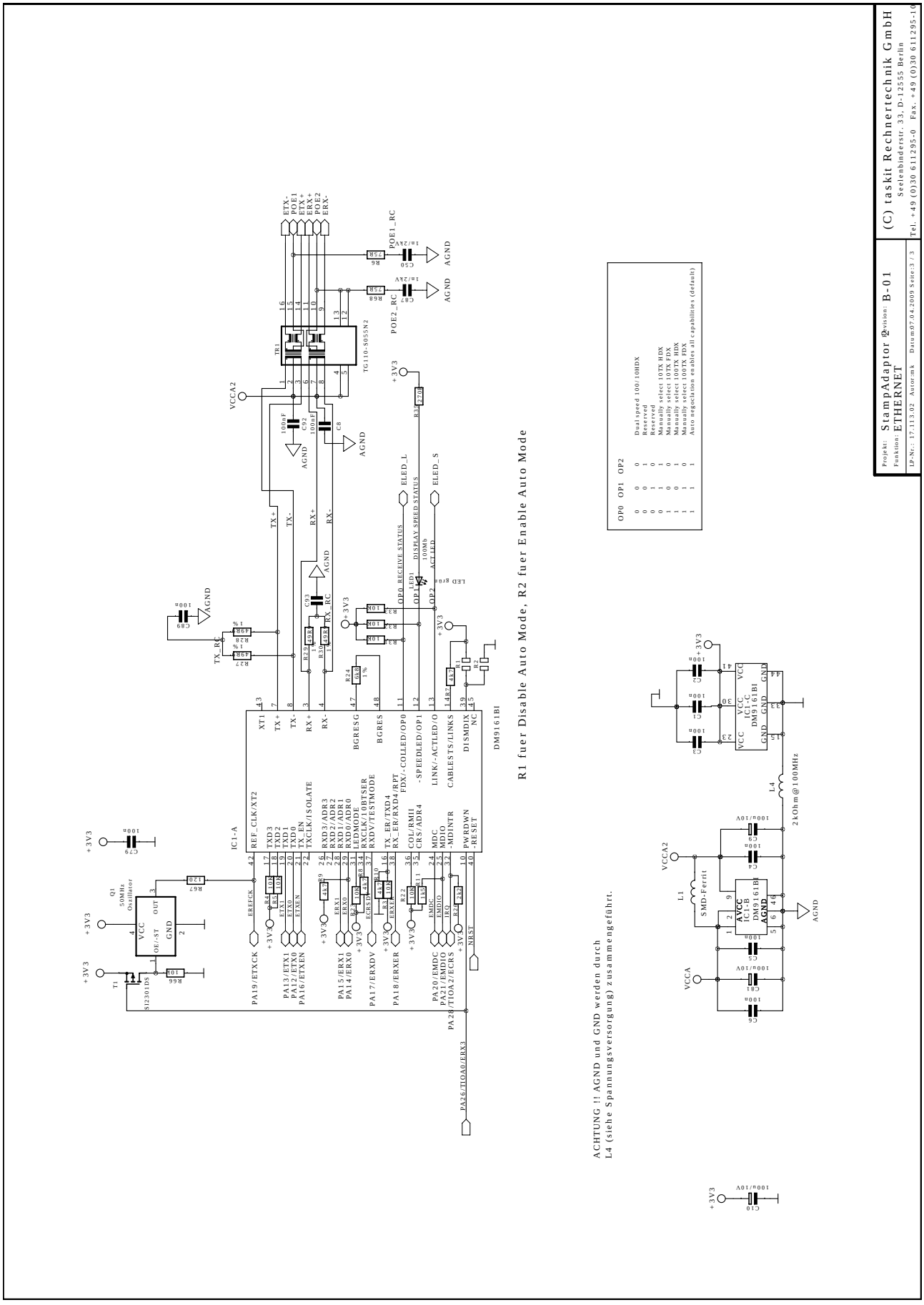

Figure 1.4. Stamp Adaptor 2 Ethernet